



**Amendment under 37 C.F.R. § 1.116  
Expedited Procedure – Art Unit 2138**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:  
Amar GUETTAF  
Appl. No.: 10/806,093  
Filed: March 23, 2004  
For: **Methods for Debugging Scan  
Testing Failures of Integrated  
Circuits**

Confirmation No.: 1879  
Art Unit: 2138  
Examiner: Radosevich, Steven D.  
Atty. Docket: 1875.4450000

***Submission Under 37 C.F.R. § 1.114(c) and Preliminary Amendment  
Under 37 C.F.R. § 1.115***

***Mail Stop RCE***

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated October 19, 2006 ("Office Action"),  
Applicant submits the following Amendment and Remarks.

Amendments to the Claims are reflected in the listing of claims which begins  
on page 2 of this paper.

Remarks and Arguments begin on page 7 of this paper.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefore (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.